

Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER

U.S. UTILITY Patent Application

(45) O.I.P.E.

PATENT DATE

SCANNED: *HKM* Q.A.

APPLICATION NO. 09/800039	CONT/PRIOR D	CLASS 422 257	SUBCLASS 371	ART UNIT 2822	EXAMINER Prenter
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10. Nerys: Bagarwala
Mehdi Zamenian
Ravi Sundaresan

APPLICANTS

TIME

CMOS integrated circuit device with LDD n-channel transistor and non-LDD p-channel transistor

PTO-2040
12/99

ISSUING CLASSIFICATION

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ 	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) (Date)		ISSUE FEE	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	

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